



LAYOUT DIAGRAM (原理圖) : (FIG-1)

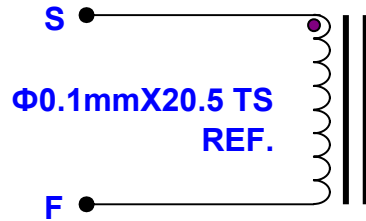


FIG-1

DATA SPECIFICATION(特性要求) : (Table-1)

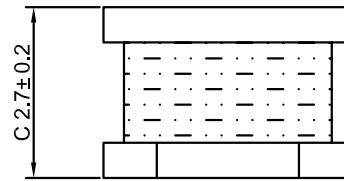
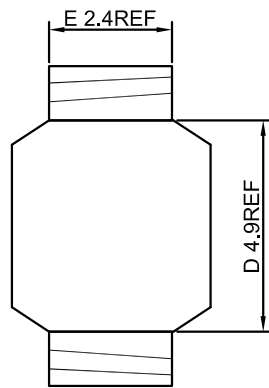
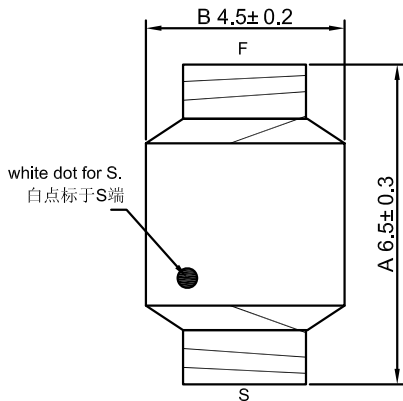
ITEM 項目	MEASUREMENTS 測試腳位	CHARACTERISTICS 測試範圍	MEASURING CONDITIONS 測試條件 T(°C)=25± 3; HR(%)=45± 15	REMARK 備注
LS 电感	PIN(S-F)	20uH±15%	F(kHz) =16 Ug(V) =0.1	SERIAL MODE 串聯模式
DCR 直流 电阻	PIN(S-F)	9.5Ω MAX.	/	4 WIRE MODE 四線模式
COPLANARITY 共平面度	ALL PINS	0.15mm MAX.		
PH 相位	REFER TO LAYOUT DIAGRAM FIG-1 參考原理圖 (FIG-1)			

Prepared By ENG:	DATE:	TITLE: SMD POWER INDUCTOR	CUSTOMER	MODEL NO:DR4.5X6.5	
Checked By QA:	DATE:		Approved:	REV.	00
Approved By ENG:	DATE:		Date:	PAGE 1 OF 2	



OUTLINE DIAGRAM (外观图) :

UNIT: mm



NOTES:

1. The part should be complied with ROHS.
产品需满足ROHS要求.

Prepared By ENG:	Date:	TITLE :	CUSTOMER	MODEL NO. :DR4. 5X6. 5	
Checked By QA:	Date:	SMD POWER INDUCTOR	Approved:	REV. :	00
Approved By ENG:	Date:		Date:	PAGE 2 OF 2	

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