

## LAYOUT DIAGRAM 原理图:(FIG-1)

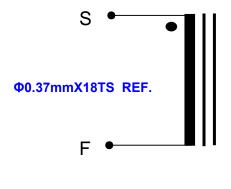
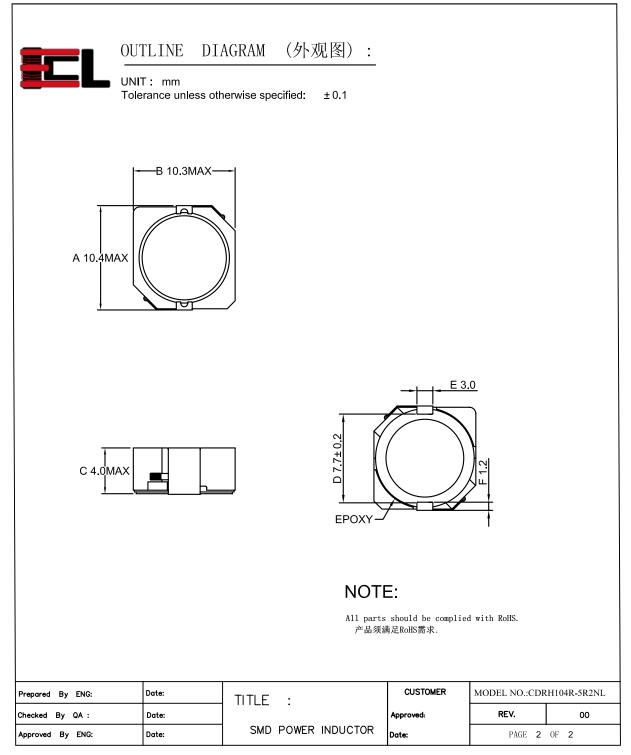


FIG-1

## DATA SPECIFICATION(特性要求): (TABLE-1)

LS 電感PIN(S-F)5.2uH±15%F(kHz)=1 Ug(V)=1SERIAL MODE 联模式DCR 直流 电阻PIN(S-F)15mΩ MAX.20 °C4 WIRES MODE 四线模式PH 相位REFER TO LAYOUT DIAGRAM (FIG-1) 參考原理圖(FIG-1)+	ITEM 项目	MEASUREMENTS 测试脚位	CHARACTERISTICS 测试范围	MEASURING CONDITIONS 条件 T=25 <u>+</u> 4℃;HR(%)=45 <u>+</u> 15	REMARK 备注			
直流 电阻PIN(S-F)15mΩ MAX.20 ℃4 WIRES MODE 四线模式PHREFER TO LAYOUT DIAGRAM (FIG-1)		PIN(S-F)	5.2uH±15%	F(kHz)=1 Ug(V)=1				
	直 流	PIN(S-F)	15mΩ MAX.	20 °C				
	相位		<i>签考</i> 尿	埋閫(FIG-1)				

Prepared By ENG:	Date:	TITLE:	CUSTOMER	MODEL NO.:CDRH104R-5R2NL	
Checked By QA:	Date:		Approved:	REV.	00
Approved By ENG:	Date:	SMD POWER INDUCTOR	Date:	PAGE 1	OF 2



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